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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/802.938

03/18/2004

Hidetaka Kaji

119146

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07/20/2007

OLIFF & BERRIDGE, PLC

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ALEXANDRIA, VA 22320

EXAMINER

HANNETT, JAMES M

ART UNIT

PAPER NUMBER

2622

MAIL DATE

DELIVERY MODE

07/20/2007

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

**Office Action Summary**

Application No.

10/802,938

Applicant(s)

KAJI, HIDETAKA

Examiner

James M. Hannett

Art Unit

2622

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 18 March 2004.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 3 is/are allowed.
- 6) ☒ Claim(s) 1 and 6-9 is/are rejected.
- 7) ☒ Claim(s) 2, 4 and 5 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 16 July 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

**1:** Claims 1 and 6-9 are rejected under 35 U.S.C. 102(e) as being anticipated by USPN

6,754,279 Zhou et al.

**2:** As for Claim 1, Zhou et al teaches on Column 2, Lines 31-42, Column 3, Lines 5-9, Column 9, Lines 58-67 and Column 10, Lines 1-27, Column 4, Lines 26-30 and depicts in Figure 1b an image data reducing device for image data composed of a plurality of components, the image data having data corresponding one-to-one to each pixel with respect to a given component among the plurality of components, and having data common to a plurality of pixels with respect to the other components, the image data reducing device (resolution reducer), comprising: a reduced image data generating circuit that receives image data that is input (150) so that data of each component (pixel) has a series relationship with each other (the data read out of the CD is read out column by column row by row and is read out in series. Therefore, the pixel data has a serial relationship), and that generates image data (106) after being reduced so as to output the image data after being reduced; and an output control signal generating circuit that generates an output control signal controlling that controls whether each component of input image data is output or not based on a decimation pattern of input component that is determined

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depending on a format of input image data and reduction ratio, the reduced image data generating circuit including a switching circuit that controls a presence of output for each component of image data input in series based on the output control signal.

3: In regards to Claim 6, Zhou et al teaches on Column 10, Lines 1-27 a reduction ratio setting register setting reduction ratio information is-being included; and reduction ratio is-being determined based on the reduction ratio information set in the reduction ratio setting register.

4: As for Claim 7, Zhou et al teaches on Column 10, Lines 1-27 a format information setting register setting format information of input image data is-being included; and a format of input image data is-being determined based on the format information set in the format information setting register.

5: In regards to Claim 8, Zhou et al teaches on Column 9, Lines 58-67 and on Column 2, Lines 29-42 a micro computer comprising the image data reducing device

6: As for Claim 9, Zhou et al teaches on Column 2, Lines 29-42 and depicts in Figure 1b an input device (150) that inputs data to be processed by the micro-computer (100); and LCD outputting device (106) that displays data that has been processed by the micro computer (100).

***Allowable Subject Matter***

7: Claim 3 is allowed.

The following is a statement of reasons for the indication of allowable subject matter: the prior art does not teach the use of the counting circuit that counts input luminance components and resets counted value in the case where the counted value reaches a reciprocal number of the reduction ratio so as to restart counting based on information about the format of the image data

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and the reduction ratio in combination with the remaining limitations of Claim 3. Therefore, the claim is allowed over the prior art.

8: Claims 2, 4 and 5 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James M. Hannett whose telephone number is 571-272-7309. The examiner can normally be reached on 8:00 am to 5:00 pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lin Ye can be reached on 571-272-7372. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James M. Hannett  
Examiner  
Art Unit 2622



JMH  
July 17, 2007